

TO SE.

LD TOP AND BOTTOM EDGES, SNAP-APART AND DISCARD CARBON

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 10-063 295		GROUP/UNIT 2817		ATTACHMENT TO PAPER NUMBER 5				
NOTICE OF REFERENCES CITED				APPLICANT(S) Abadeer, et al.								
U.S. PATENT DOCUMENTS												
		DOCUMENT NO.				DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE		
A	5	4	4	8	20	5	9-5-1995	Re thermal	331	57		
B	6	2	7	2	4	3	9	8-7-2001	Buer et al.	327 158		
C	6	4	7	6	6	3	2	11-5-2002	La Rosa et al.	324 769		
D												
E												
F												
G												
H												
I												
J												
K												
FOREIGN PATENT DOCUMENTS												
		DOCUMENT NO.				DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
L	20	03	9	1	9	9	1	3-25-2003	Japan	Hur et al.	—	—
M												
N												
O												
P												
Q												
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)												
R	Srivastava et al. "InP HBT Ring Oscillator with Zener stage gate delay" Gallium Arsenide Integrated Circuit Symposium 2002, 24 th Annual Technical Digest Oct 20-25, 2002 pp 171-174											
T												
U												
EXAMINER MICHAEL B SINGLETON						DATE 7-1-03						
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)												